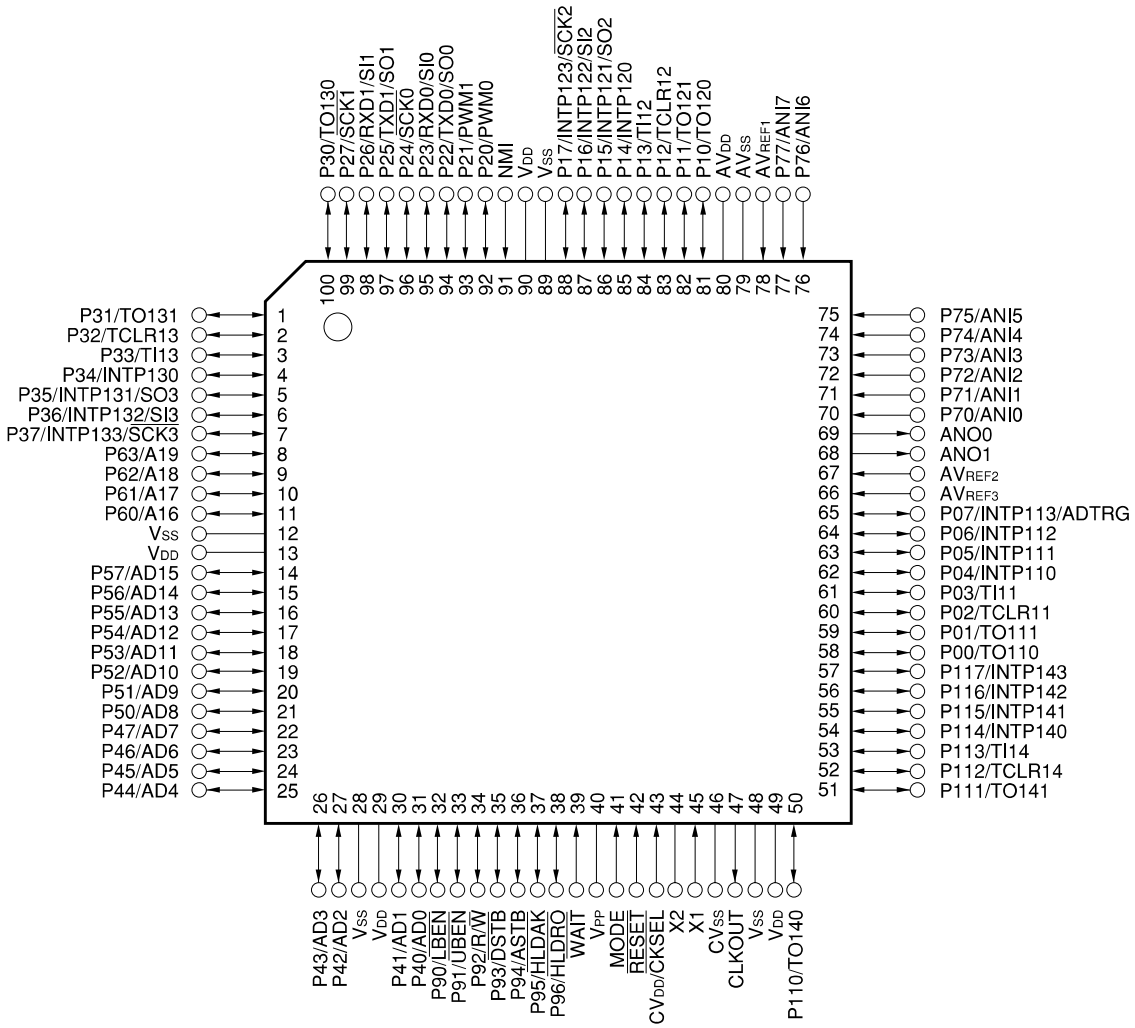
APPLICATIONS

μPD70F3003A, 70F3025A: Camcorders, VCRs, PPCs, LBPs, printers, motor controllers, NC machine tools, mobile telephones, etc.

μPD70F3003A(A): Medical equipment, automotive appliances, etc.

★ PIN CONFIGURATION (Top View)

- **100-Pin Plastic LQFP (fine pitch) (14 × 14)**
  μPD70F3003AGC-33-8EU **μPD70F3025AGC-33-8EU**
  μPD70F3003AGC-33-8EU-A **μPD70F3025AGC-33-8EU-A**
  μPD70F3003AGC(A)-33-8EU



Caution Connect VPP pin to VSS pin except the case that μPD70F3003A, 70F3003A(A) or 70F3025A is used in flash memory programming mode.

PIN NAMES

A16 to A19:	Address bus
AD0 to AD15:	Address/data bus
ADTRG:	A/D Trigger input
ANI0 to ANI7:	Analog input
ANO0, ANO1:	Analog output
ASTB:	Address strobe
AV_{DD}:	Analog V_{DD}
AV_{REF1} to AV_{REF3}:	Analog reference voltage
AV_{SS}:	Analog V_{SS}
CV_{DD}:	Power supply for clock generator
CV_{SS}:	Ground for clock generator
$\overline{CKSEL}$:	Clock select
CLKOUT :	Clock output
$\overline{DSTB}$:	Data strobe
$\overline{HLDAK}$:	Hold acknowledge
$\overline{HLDRQ}$:	Hold request
INTP110 to INTP113, INTP120 to INTP123, INTP130 to INTP133, INTP140 to INTP143:	Interrupt request from peripherals
$\overline{LBEN}$:	Lower byte enable
MODE:	Mode
NMI:	Non-maskable interrupt request
P00 to P07:	Port 0
P10 to P17:	Port 1
P20 to P27:	Port 2
P30 to P37:	Port 3
P40 to P47:	Port 4
P50 to P57:	Port 5
P60 to P63:	Port 6
P70 to P77:	Port 7
P90 to P96:	Port 9
P110 to P117:	Port 11
PWM0, PWM1:	Pulse width modulation
$\overline{RESET}$:	Reset
R/$\overline{W}$:	Read/write status
RXD0, PXD1:	Receive data
$\overline{SCK0}$ to $\overline{SCK3}$:	Serial clock
SI0 to SI3:	Serial input
SO0 to SO3:	Serial output
TO110, TO111, TO120, TO121, TO130, TO131, TO140, TO141:	Timer output
TCLR11 to TCLR14:	Timer clear
TI11 to TI14:	Timer input
TXD0, TXD1:	Transmit data
$\overline{UBEN}$:	Upper byte enable
$\overline{WAIT}$:	Wait
X1, X2:	Crystal
V_{DD}:	Power supply
V_{PP}:	Programming power supply
V_{SS}:	Ground